	Type	L #	Hits	Search Text	DBs	Time Stamp
1	IS&R	L1	1007	(712/235,237-240).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2006/01/12 16:18
2	BRS	L2	410	1 and 'branch target address'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2006/01/12 16:19
3	BRS	L3	0	2 and predictind near 'target address'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2006/01/12 16:21
4	BRS	L4	49	2 and predicting near3 'target address'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2006/01/12 16:21
5	BRS	L5	0	'branch target address' and (predicting near3 'target address') and 'selecting logic'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2006/01/12 16:22
6	BRS	L6	8	'branch target address' and (predicting near3 'target address') and 'selection logic'	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2006/01/12 16:23

7	BRS	L7	5	'branch target address' and (predicting near3 'target address') and 'selection logic' and instruction adj byte	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2006/01/12 16:23

	Type	L #	Hits	Search Text	DBs	Time Stamp
8	BRS	L8	2	('branch target address' and (predicting near3 'target address') and 'selection logic' and instruction adj byte).clm.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	2006/01/12 16:24
9	BRS	L9	3	('branch target address' and (predicting near3 'target address') and 'selection logic' and instruction adj byte)	US-PGPUB	2006/01/12 16:24
10	BRS	L10	2	('branch target address' and (predicting near3 'target address') and 'selection logic' and instruction adj byte and instruction adj buffer)	US-PGPUB	2006/01/12 16:24
11	BRS	L11	1	('branch target address' and (predicting near3 'target address') and 'selection logic' and instruction adj byte and instruction adj buffer).clm.	US-PGPUB	2006/01/12 16:24